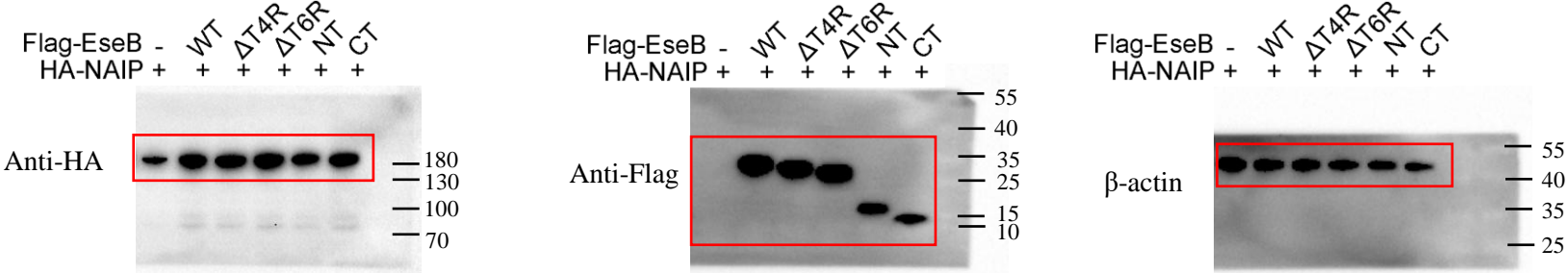


Figure 4I – source data-annotated

Input



IP

