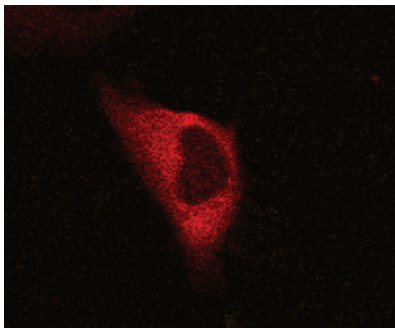
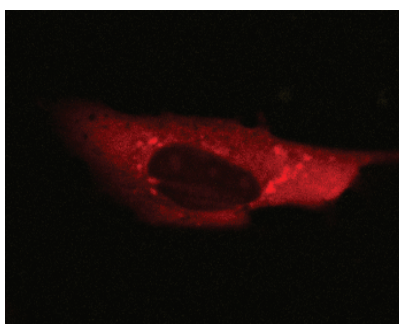


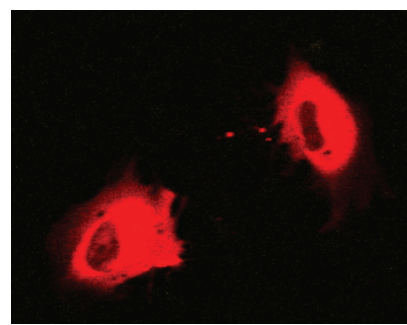
**A** VN-CLOCK + VC-MATR3



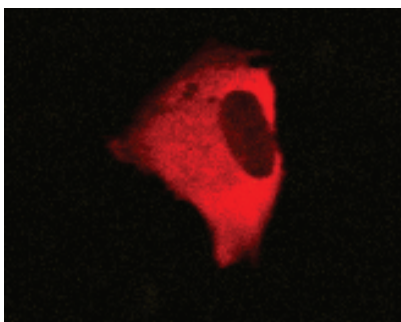
**B** VC-BMAL1 + VN-MATR3



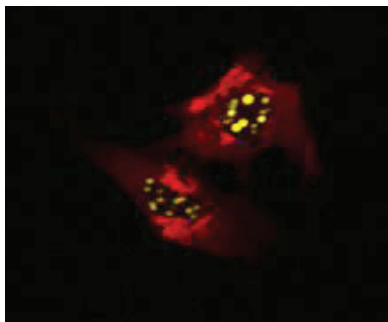
**C** VN-HDAC5 + VC-vector



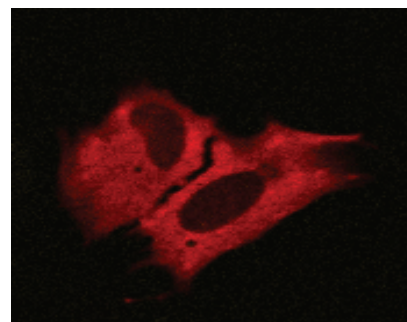
**D** VC-MEF2c + VN-vector



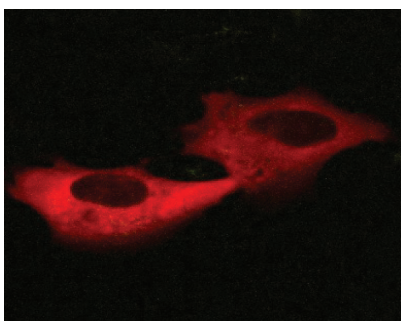
**E** VN-HDAC5 + VC-MEF2c



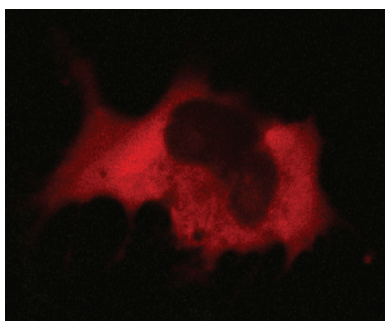
**F** VN-BMAL1 + VC-vector



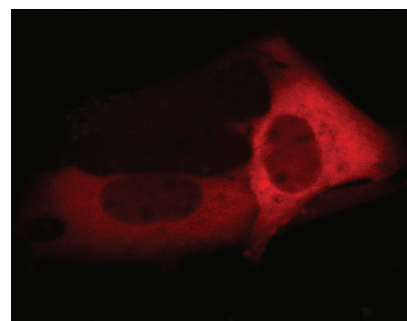
**G** VC-BMAL2 + VN-vector



**H** VC-MYOD1 + VN-vector



**I** VN-CLOCK + VC-vector



**J** VN-BMAL1 + VC-vector

