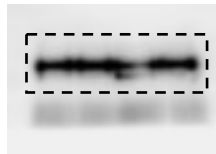


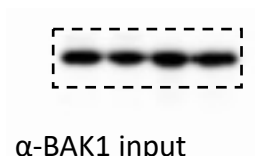
Source Data Fig. 2A



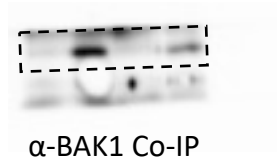
α -FLS2 input



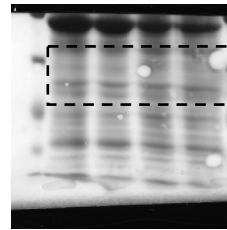
α -FLS2 IP



α -BAK1 input



α -BAK1 Co-IP



CBB input