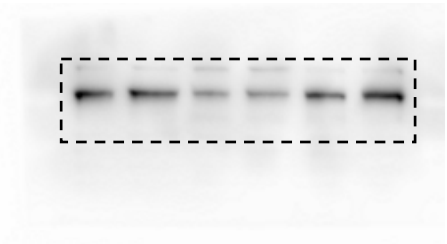
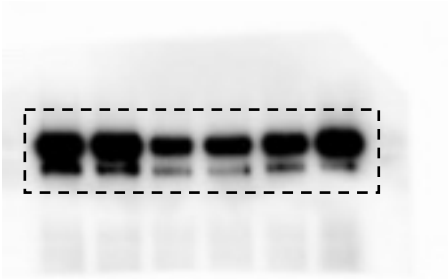
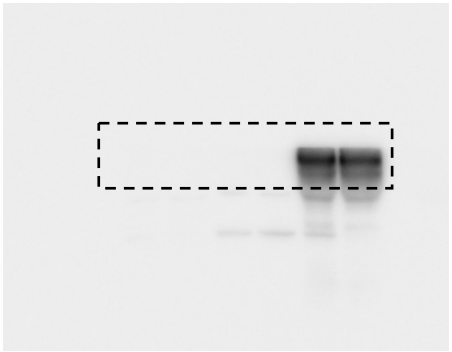
Source Data Figure 3 – supplement figure 1



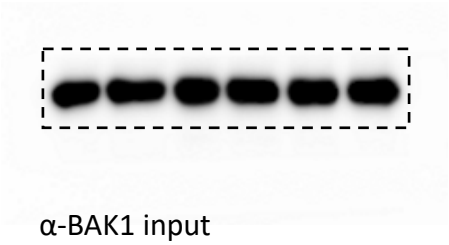
α-FLS2 input



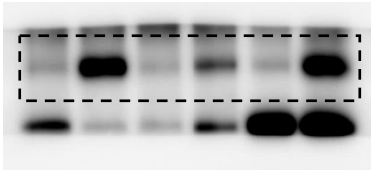
α-FLS2 IP



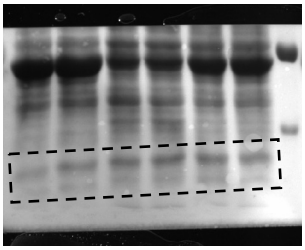
α-FLAG input



α-BAK1 input



α-BAK1 Co-IP



CBB input