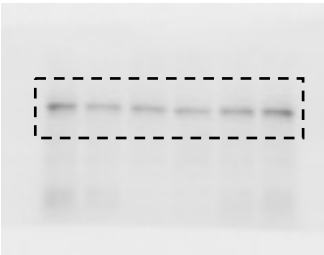


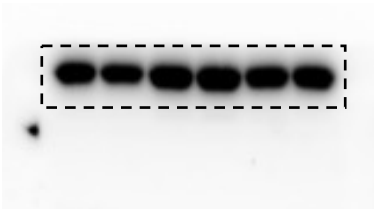
Source Data Figure 4 – supplement figure 1



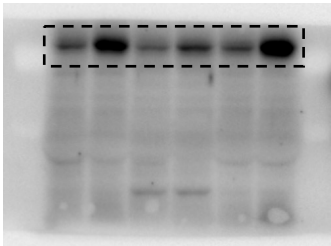
α -FLS2 input



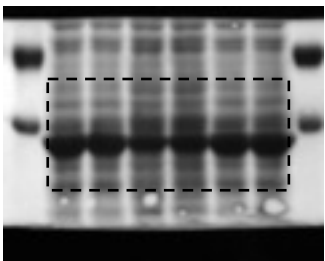
α -FER input



α -BAK1 input



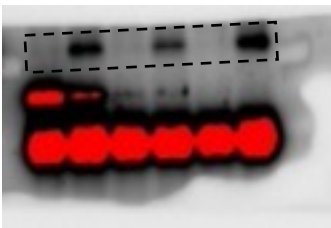
α -BAK1 pS612 input



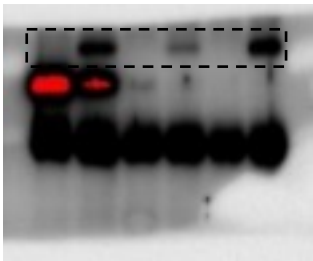
CBB input



α -FLS2 IP



α -BAK1 Co-IP



α -BAK1 pS612 Co-IP