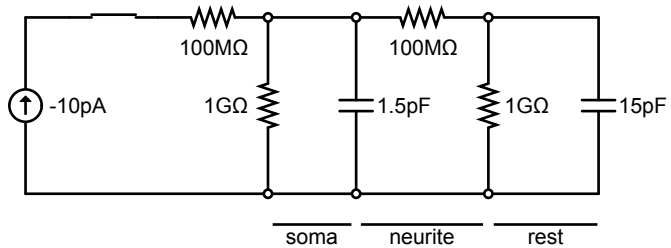
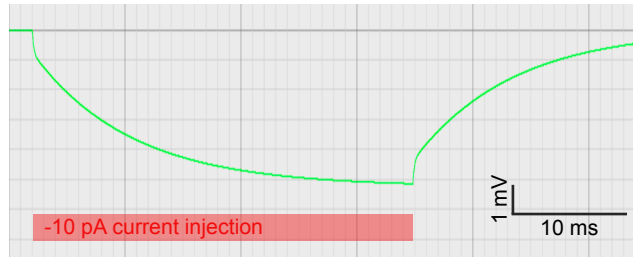


**A**

circuit model

**B**

modeled voltage

**C**

cell 1

